



PATENT

Docket No.: 12290/1

#3
BT
4-6-02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS : Yoshikazu MIYANAGA et al.
SERIAL NO. : 10/026,524
FILED : 27 December 2001
FOR : SPEECH RECOGNITION DEVICE
GROUP ART UNIT : 2641

ASSISTANT COMMISSIONER
FOR PATENTS
Washington, DC 20231

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**INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. § 1.97 & § 1.98**

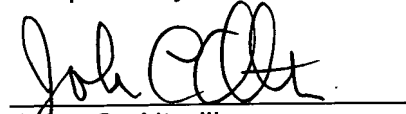
S I R:

In conformance with Applicant's duty of disclosure under 37 C.F.R. § 1.56 and § 1.97(b)(3), the references listed on the attached form PTO-1449 are hereby brought to the Examiner's attention.

In accordance with the requirements of 37 C.F.R. § 1.98, copies of the references are submitted herewith.

It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear in the "references cited" on any patent to issue therefrom.

Respectfully submitted,


John C. Altmiller
(Reg. No. 25,951)

Date: 28 March 2002

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**FORM PTO-1449
INFORMATION DISCLOSURE
STATEMENT BY APPLICANT(S)**

Atty Docket No. : 12290/1
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Inventors : Y. MIYANAGA et al.
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Examiner :

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OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.) **Technology Center 2600**

Examiner
Initial

1. Y. MIYANAGA et al.: *On Generalization and Adaptation of a Self-Organized Clustering*, Electronic Information/Communication Conference (A), Vol. J75-A, No. 7, pp. 1207-1215, July 1992 and English translation thereof
2. Y. MIYANAGA et al.: *On High Speed and High Accuracy Learning of a Network Using Self-Organization and Supervision*, Electronic Information/Communication Conference (A) Vol. J78-A, No. 11, pp. 1475-1484, Nov. 1995 and English translation thereof
3. Rafiqul ISLAM et al.: *Multi-clustering Network for Data Classification System*, IEICE TRANS. FUNDAMENTALS, Vol. E80-A, No. 9, pp. 1647-1654, Sep. 1997
4. Masahiro KONDA et al.: *Neuron-MOS Correlator Based on Manhattan Distance Computation for Event Recognition Hardware*, IEEE International Symposium on Circuits and Systems, Vol. 4, Atlanta, USA, pp. 217-220, May 1996

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



Sheet 2 of 2

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- _____ 5. Ugur Cilingiroflu et al.: *A 4-Transistor Euclidean Distance Cell for Analog Classifiers*, IEEE International Symposium on Circuits and Systems, Vol. 1, California, USA, pp. 84-87, May 1998
- _____ 6. M. KABASAWA et al.: *An Analog Design of a Clustering System by using C-matrix Structure*, Technical Report of IEICE, CAS2000-86 (2001-01), pp. 57-64
- _____ 7. M. KABASAWA et al.: *A Clustering/Labeling Analog LSI Architecture for Media Recognition*, Electronic Information/Communication Conference (A), Vol. J84-A, No. 4, pp. 456-461, April 2001

EXAMINER

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